

Notice of Allowability

Application No.

10/693,926

Applicant(s)

HUNG, CHI-CHENG

Examiner

Art Unit

Craig E. Walter

2188

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed on 28 March 2006.
2. ☒ The allowed claim(s) is/are 1-20 (as per original numbering).
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

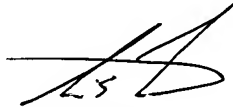
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


Craig E. Walter
Examiner, Art Unit 2188

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given in a telephone interview with Mr. Kenneth Berner on 18 April 2006.

3. Please amend claim 1 to read as follows:

A micro-system for burning-in a system program from a plug-able subsystem into a main memory, comprising:

a processor for sending out a first control signal and a second control signal;

a main memory connected electrically with said processor for transferring data between said processor and said main memory;

a device for adjusting to a first logic-level connected electrically with said processor for receiving said first control signal and connected electrically with said main memory for changing a logic-level of a third control signal to said first logic-level to enable said main memory; and

a plug-able subsystem with a backup memory connected electrically with said processor for receiving said second control signal, wherein data are transferred between said backup memory and said processor when said second control signal is at said first logic-level, and said plug-able subsystem sends out a

forth control signal to said main memory for changing said third control signal to a second logic-level to disable said main memory, and the data are not transferred between said backup memory and said processor when said second control signal is at said second logic-level, and said third control signal is at said first logic-level to enable said main memory for transferring data between said processor and said main memory.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

REASONS FOR ALLOWANCE

5. The following is an examiner's statement of reasons for allowance:

Kokubun et al. (US PG Publication 2002/0023198 A1), hereinafter Kokubun teaches an information processing apparatus and data backup method, which comprises a primary apparatus connected to a secondary storage backup device. The apparatus further includes a detection unit, which enables the system to detect if and when the secondary device is connected to the apparatus. The system uses the detection control signal to determine if the backup device is present, and further determines where to store the data (either in the primary or secondary backup location) based on the detection signal - paragraph 0010, all lines.

Nagaishi (US Patent 5,291,425) teaches a test mode setting arrangement for use in microcomputer, which includes a system comprising a CPU that acquires access to either an internal or external memory device depending on the logic level of a control signal. Nagaishi further teaches the use of a flip-flop, which issues an output signal to a device that produces two signals required to select access to either the internal or external memory (col. 3, lines 14-39).

Despite these teachings, neither Kokubun nor Nagaishi teach (either alone or in combination), the use of four control signals, in which the first control signal changes a third control signal to enable access to a memory, and likewise changes a fourth control signal in order to control a third control signal in order to disable the memory access as recited by Applicant in independent claims 1 and 11.

Claims 2-10 and 12-20 are allowable for further limiting independent claims 1 and 11 respectively.

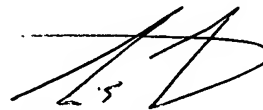
Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Craig E. Walter whose telephone number is (571) 272-8154. The examiner can normally be reached on 8:30a - 5:00p M-F.
7. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on (571) 272-4210. The fax phone

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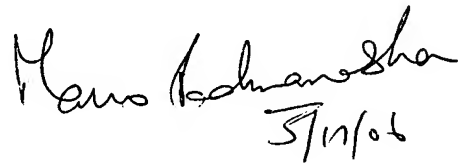
number for the organization where this application or proceeding is assigned is 571-273-8300.

8. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



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SUPERVISORY PATENT EXAMINER